

Abstracts

Designing a 100 MHz SPARC Dual Processor Using MCM- L Packaging Technology and Microwave Design Techniques

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This paper presents typical microwave design techniques applied to designing high speed MCMs for digital applications. Significantly improved MCM performance and reduced number of design iterations are achieved, by using these techniques. Further, a design of 100 MHz SPARC dual processor using MCM- L technology is presented, taking advantage of using unpackaged chips and design techniques frequently used in microwave designs.

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